

L1CFT-L1MU Commissioning

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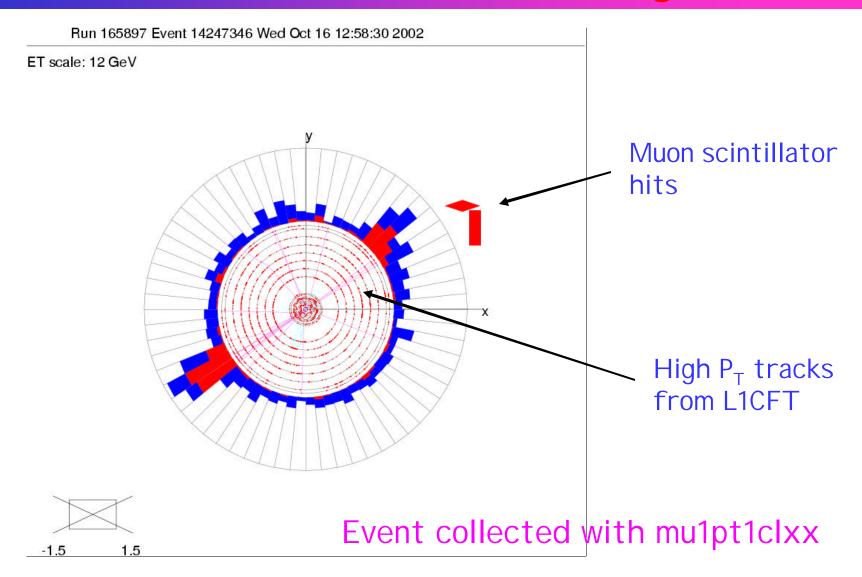


Status

- All central L1CFT-L1MU cables tested
 - One problem xmitter on sector 69
 - ◆ ~6/388 suspect splitter -> MTCxx
- Small amount of data collected with L1CFT.L1MU trigger
 - Good results seen in event displays
- GUI's constructed to easily allow shifters to disable all/some of the L1CFT inputs
- DD/AS OK'd two stores of running with L1CFT inputs enabled
 - ◆ Ran all owl shift last night with no problems, will run again tonight
 - ◆ Important because we want to see if there are any gross L1CFT input problems during typical store
- Central and forward L1CFT.L1MU FPGA logic ready to go (but possibly not optimized)
 - Need central bottom



L1CFT. Scintillator Commissioning





Shifter GUI (Crate Reload)

Crate controls	PDT masks	MDT masks	SCI masks	CFT masks					
				L1 Muo	n Crate	Panel			
		crate id		crate commands		crate status			
		MCNNA	cold start	restore	check	oriline	not touched		
		MCNNB	cold start	restore	check	offline	not touched		
		MCNSA	cold start	restore	check	urifline	not touched		
		MCNSB	cold start	restore	check	ufflice	not touched		
		MCNC	cold start	restore	check	offline	not touched		
		мтес	cold start	restore	check	offline	not touched		
		MTCN	cold start	restore	check	offline	not touched		
		MTCS	cold start	restore	check	offline	not touched		
		МТМ	cold start	restore	check	offline	not touched		
				cold start a	ll crates				
				eo	mmand status				
	reset ma	asks		write triç	gger ma	sk files	exit no write		



Shifter GUI (L1CFT Input Disable)

Crate controls	PDT masks	MDT masks	SCI	nasks	CFT	nasks								
CFT test config														
		00	tant 0	W 0	# 1	z	# 3	# 4	= 5	# 6	# 7	w a	# 9	
		00	tant 1	II 10	# 11	II 12	II 13	W 14	II 15	JE 16	II 17	II 18	JE 19	
		00	tant 2	20	= 21	≡ SS	≡ 23	II 24	2 5	36	= 27	JE 28	≡ 29	
		00	tant 3	W 30	J 31	II 32	■ 33	₩ 34	35	36	37	⊯ 38	₩ 39	
		00	tant 4	# 40	= 41	■ 42	# 43	4 4	4 5	# 46	47	# 48	# 49	
		00	tant 5	# 50	# 51	≡ 52	J 53	# 54	# 55	# 56	= 57	⊯ 58	# 59	
		00	tant 6	= 60	# 61	# 62	≡ 63	6 4	6 5	# 66	67	≡ 68	⊿ 69	
		00	tant 7	# 70	= 71	# 72	7 3	# 74	7 5	# 76	= 77	# 78	79	
			kill all cft inputs											
2	reset masks					write trigger mask files						exit no write		



- These are the same tasks that we have followed in certifying scintillator and wire triggers
- Important for Lipton et al to press DØ management to allow L1CFT inputs to be enabled during stores
 - Probably some loss of data-taking efficiency but increases rate of progress in L1CFT-L1MU certification
- Stefan Anderson (postdoc) slated to lead this effort
 - With help from Rob, Noah, Jeff, KJ



- Produce MC efficiencies for L1CFT.L1MU triggers
 - Waiting for p13.03 geometry
 - Eventually we will rewrite this section of tsim_l1muo to make it more RCP-based
- Produce data efficiencies and purity for L1CFT.L1MU triggers
 - ◆ Eff == L1MU trigger/Reco muon for jet triggers
 - Root-tuple analysis code ready to go
 - ◆ TMB analysis code must wait for p13.04 (or later) when trigger chunk is included (Kyle Stevenson)



- Measure L1CFT.L1MU rates and rejections
 - ◆ Easily done if L1CFT inputs enabled
 - Also measure rates for each sector and each octant
 - Trigger Rate (Hz)

 ++++++++

 pt1 loose 350

 pt2 loose 45

 pt3 loose 10

 pt4 loose 2

 pt2 tight 3

 pt3 tight 1

 pt4 tight 0.2

 pt4 only 65
- Collect nice event displays with L1CFT.L1MU trigger
 - I mpress your colleagues
 - More importantly, study events to optimize trigger (improve efficiency and/or rejection)



- Hardware-Simulator comparisons
 - Compare hardware result with raw data run through tsim_l1muo result
 - ◆ Running tsim_I1I2 (p13.02) online
 - Track down discrepancies (but for scintillator and wire triggers there was >90% agreement from the start)
- Complete L1CFT-L1MU cable repairs



Conclusions

- L1CFT.L1MU commissioning plan is the same one we followed in successfully commissioning scintillator and wire triggers
- Can proceed once L1CFT inputs are enabled
 - Help from CFT shifters to monitor? I.e., checking for good synch gap at DFEA via DFE_Ware
 - SCL Inits after CFT downloads (to re-synch inputs at L1Muo)
- Data analysis using TMB must be developed
 - ◆ Increase commissioning time